

REMARKS

Applicant appreciates the Examiner's thorough consideration provided the present application. Claims 1, 3-5 and 7-13 are now present in the application. Claims 1, 5 and 10 have been amended, claims 11-13 have been added, and claims 2 and 6 have been cancelled by the present amendment. Claims 1, 5 and 10 are independent. Reconsideration of this application, as amended, is respectfully requested.

In the outstanding Office Action, claims 1-3, 5-7, 9 and 10 were rejected under 35 U.S.C. § 103(a) as unpatentable over Applicant's admitted prior art (AAPA) in view of Suominen et al. and Nishimura; and claims 4 and 8 were rejected under 35 U.S.C. § 103(a) as unpatentable over (AAPA) in view of Suominen et al. Nishimura and Tsuda.

Applicant thanks the Examiner for discussing this application with Applicant's representative on June 14, 2007. During the discussion, the differences between the present invention and the applied art were explained to the Examiner. No agreement was reached pending the Examiner's further review when a response is filed. Comments presented during the discussion are reiterated below.

Claims 1-3, 5-7, 9 and 10 stand rejected under 35 U.S.C. § 103 (a) as unpatentable over (AAPA) in view of Suominen et al. and Nishimura. This rejection is respectfully traversed.

During the discussion on June 14, 2007, the Examiner indicated that Fig. 2 of the present invention is similar to Fig. 1 (AAPA), with the exception of the memory 9 and error correction stage 10, and requested the claims be amended to clearly distinguish the features of the present invention from Fig. 1 of AAPA. Accordingly, independent claims 1, 5 and 10 have been amended to clearly distinguish from Fig. 1 (AAPA). In more detail, the differences between Fig.

1 (AAPA) and the present invention can be best seen by comparing Figs. 1 and 2. As shown in Fig. 1 (AAPA), only the signal for the demodulator 2 and the reference filter 13 passes the receive filter 1, and the signal for the weighting filter 11 is separated at the input of the receive filter 1 and is fed to the memory 9 and error correction stage 10 before passing to the weighting filter 11. That is, the memory 9 and error correction stage 10 are required in the arrangement shown in Fig. 1.

The present invention advantageously removes the requirement for the memory 9 and error correction stage 10 of Fig. 1 by using the specific weighting filter function as determined by the convolution operation relationship: $\text{weighting filtering} = \text{receive filtering} * \text{measuring filter}$. Because of this advantageous convolution operation, the first measuring signal is directly passed from the demodulator to the measuring filter without having been delayed in a memory prior to being input to the demodulator. It is respectfully noted the weighting filter 11 in Fig. 1 (AAPA) is designed in accordance with a desired weighting function, for example, in accordance with the ETSI specification. Further, the input signal to be weighted in the weighting filter 11 has to first be delayed in a memory 9 and error corrected in an error correction stage 10 connected to the demodulator 2, before being fed to the weighting filter 11. This is because the weighting filter 11 in the Fig.1 (AAPA) does not include the same convolution operation in the measuring filter 22 as in the present invention. That is, because of the advantageous convolution operation performed in the measuring filter 22, the memory 9 is not needed and the first measuring signal can be directly passed from the demodulator to the measuring filter without having been delayed in a memory prior to being input to the demodulator.

Further, combining Suominen et al. and Nishimura et al. with AAPA of Fig.1 also does not result in the advantageous convolution operation performed in the measuring filter 22. Thus, none of the applied art teaches or suggest the advantageous convolution operation defined in the claims such that the first measuring signal can be directly passed from the demodulator to the measuring filter without having been delayed in the memory prior to being input to the demodulator.

Accordingly, it is respectfully submitted independent claims 1, 5 and 10 and each of the claims depending therefrom are allowable.

Further, it is respectfully submitted the rejection of claims 4 and 8 under 35 U.S.C. § 103(a) as noted in the Office Action has also been overcome as the claims rejected therein are dependent claims and Tsuda also does not teach or suggest the features recited in the corresponding independent claims.

In addition, new claims 11-13 have been added to set forth the invention in a varying scope, and Applicant submits the new claims are supported by the originally filed specification. For example, as shown in Fig. 2 of the present application, the digitally modulated signal input to the receive filter is not split into two signals. This differs from Fig. 1 of AAPA in which the signal input to the receive filter 1 has to first be split into two signals and then one of the signals is passed to the memory 9 so as to be delayed before being error corrected in the error correction stage 10 and weighted in the weighting filter 11. Therefore, it is respectfully submitted these claims further define over the applied art.

CONCLUSION

All the stated grounds of rejection have been properly traversed and/or rendered moot. Applicant therefore respectfully requests that the Examiner reconsider all presently pending rejections and that they be withdrawn.

It is believed that a full and complete response has been made to the Office Action, and that as such, the Examiner is respectfully requested to send the application to Issue.

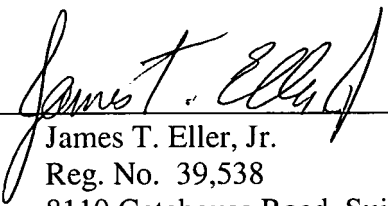
In the event there are any matters remaining in this application, the Examiner is invited to contact David A. Bilodeau, Reg. No. 42,325, at (703) 205-8072 in the Washington, D.C. area.

If necessary, the Commissioner is hereby authorized in this, concurrent, and future replies, to charge payment or credit any overpayment to Deposit Account No. 02-2448 for any additional fees required under 37 C.F.R. §§1.16 or 1.17; particularly, extension of time fees.

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Respectfully submitted,

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